

ABSTRACT OF THE DISCLOSURE

Driver Circuit

A circuit includes a first driver, a second driver, and a transformer coupled to the first and second driver. In operation, the first driver receives a first signal from a first input port, the second driver receives a time-delayed version of the first signal from a second input port, and the transformer provides provide an output signal to an output port. A method includes receiving a first input signal, receiving a second input signal, and then processing the first input signal and the second input signal. The second input signal is a time-delayed version of the first input signal and the processing of the first input signal and the second input signal generates a half-raised cosine signal.

"Express Mail" mailing label number: EV 332569180 US
Date of Deposit: December 29, 2003
This paper or fee is being deposited on the date indicated above with
the United States Postal Service pursuant to 37 CFR 1.10, and is
addressed to the Commissioner for Patents, Mail Stop Patent
Application, P.O. Box 1450, Alexandria, VA 22313-1450.